

Amendments to the claims:

1 (canceled)

2 (original): A digital power amplifier comprising:  
an analog amplifier which amplifies an input analog signal;  
a low-pass filter including a coil and a first capacitor; and  
a digital amplifier block which converts the output of said analog amplifier to a PWM signal, and controls power supply to said low-pass filter;  
wherein a series circuit comprising a second capacitor and a resistance is applied as a feedback circuit which feeds-back a node voltage between the coil and the first capacitor of said low-pass filter to said analog amplifier, and the series circuit has a damper function for damping a high pass peak in the frequency response characteristic of said low-pass filter, which occurs when a load is not connected to said low-pass filter, or a high impedance load is connected thereto.

3 (new): A digital power amplifier as claimed in claim 2 wherein said digital amplifier block includes:  
a PWM generator; and  
a switching section controlled by the PWM generator wherein the switching section has a first switching element, a first coil, a second coil and a second switching element, connected in series in this order, between a high potential power supply line and a low potential power supply line;

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said first and second switching elements being unipolar transistors which operate complementarily, and when changing the states of the switching elements, dead time during which the both switching elements are switched off is provided, to control power supply to a low-pass filter;

said switching section further including a first high-speed diode in which a cathode is connected to said high potential power supply line, and an anode is connected to a node between said second coil and said second switching element, and a second high-speed diode in which a cathode is connected to a node between said first switching element and said first coil, and an anode is connected to said low potential power supply line, and the node between said first coil and said second coil being connected to said low-pass filter.

4 (new): A digital power amplifier as claimed in claim 2 wherein said analog amplifier includes an operational amplifier having a non-inverting receiving the input analog signal and an inverting input to which the feedback circuit is connected.

5 (new): A digital power amplifier as claimed in claim 3 wherein said analog amplifier includes an operational amplifier having a non-inverting receiving the input analog signal and an inverting input to which the feedback circuit is connected.